

Electronic Patent Application Fee Transmittal

Application Number:	10016449			
Filing Date:	10-Dec-2001			
Title of Invention:	Method and system for use of a field programmable gate array (FPGA) function within an application specific integrated circuit (ASIC) to enable creation of a debugger client within the ASIC			
First Named Inventor/Applicant Name:	Robert Thomas Bailis			
Filer:	Joseph A. Sawyer			
Attorney Docket Number:	RPS920010127US1			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Total in USD (\$)				810